

Digital Electronics

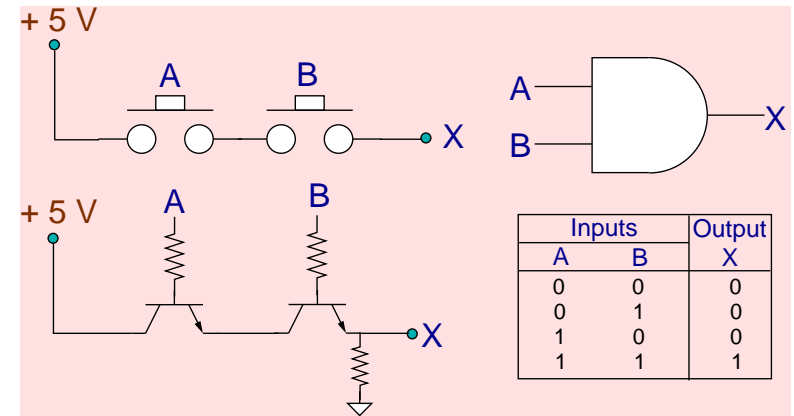
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Bangladesh University of Engineering & Technology

ME 361: Instrumentation & Measurement



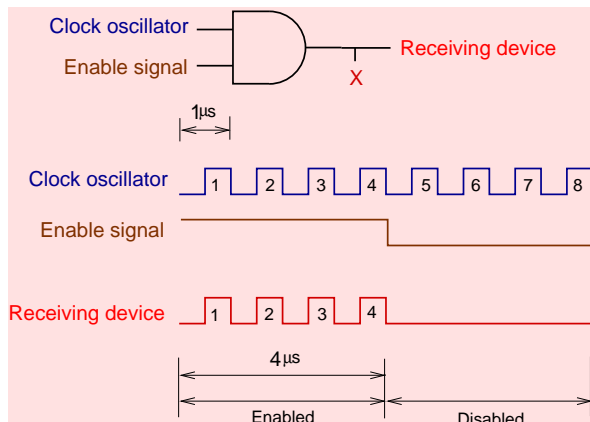
AND Gate



- Output, X, is HIGH only if inputs A and B are both HIGH.
- Boolean Equation: $X = A \text{ AND } B = A \cdot B = AB$



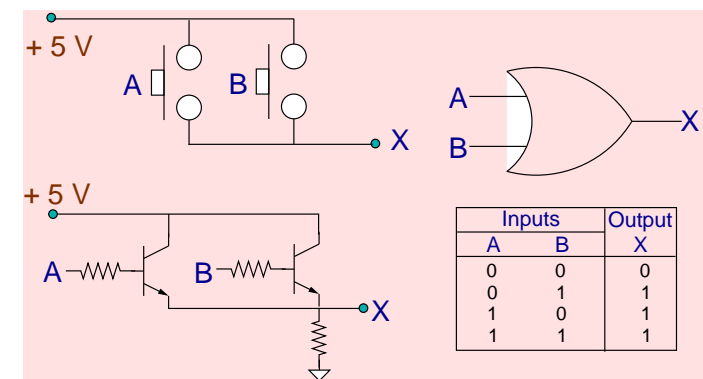
... contd.



- AND gate can be used to **enable** a waveform to transmit from one point to another. The LOW value disables the clock from reaching the X-output.



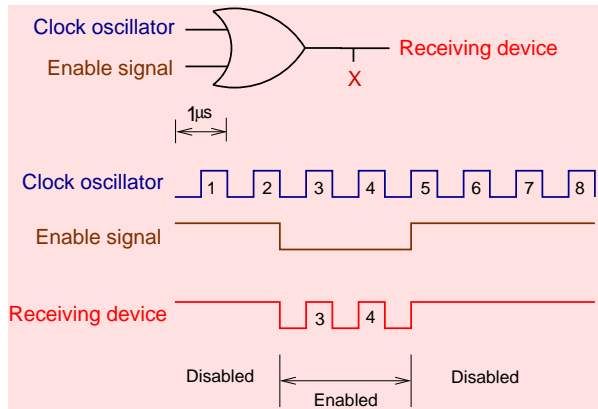
OR Gate



- Output, X, is HIGH if input A or input B is HIGH or both are HIGH.
- Boolean Equation: $X = A \text{ OR } B = A + B$



... contd.

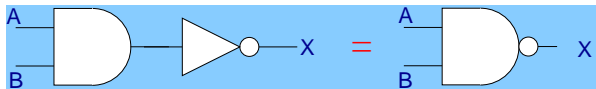


- OR gate can be used to **disable** a waveform from transmitting from one point to another.

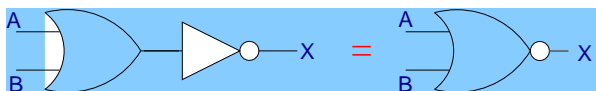


NAND & NOR Gates

- NAND:** Boolean Equation: $X = \overline{AB}$
- Output is always HIGH unless both inputs are HIGH.

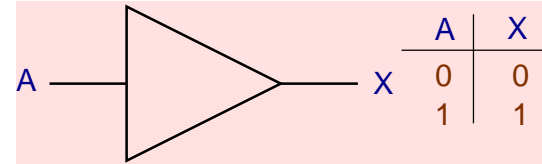


- NOR:** Boolean Equation: $X = \overline{A + B}$
- Output is always LOW unless both inputs are LOW.

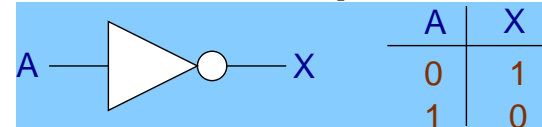


Buffer & Inverter ICs

- Buffer IC:** Boolean Equation: $X = A$



- Inverter IC:** Boolean Equation: $X = \overline{A}$



Ex-OR/Ex-NOR Gates

- Exclusive-OR (Ex-OR) gate provides a HIGH output if one input or the other input is HIGH, but not the both.

Ex-OR:	$X = A \oplus B = \overline{A}B + A\overline{B}$
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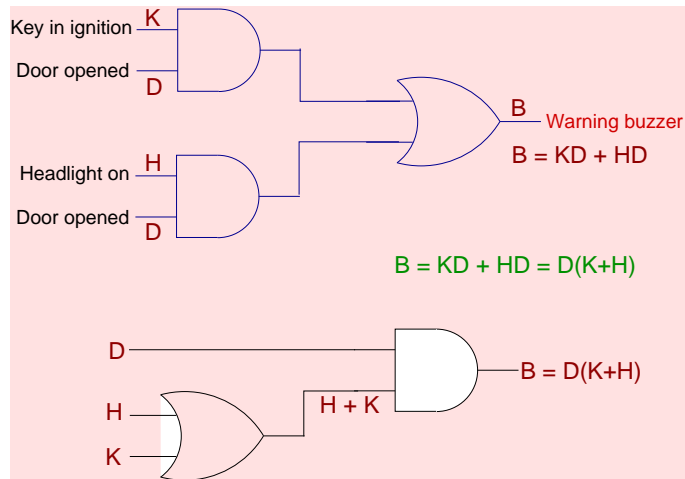
- Ex-NOR is the compliment of the Ex-OR. It provides a HIGH output for both inputs HIGH or both inputs LOW.

Ex-NOR:	$X = \overline{A \oplus B} = AB + \overline{A}\overline{B}$
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A	B	X(OR)	X(Ex-OR)	X(Ex-NOR)
0	0	0	0	1
0	1	1	1	0
1	0	1	1	0
1	1	0	0	1



Combinational Logic Example



Boolean Algebra Laws

- Commutative law of addition: $A + B = B + A$, and multiplication: $AB = BA$.
These laws mean that the order of ORing and ANDing does not matter.
- Associative law of addition: $A + (B + C) = (A + B) + C$, and multiplication: $A(BC) = (AB)C$.
These laws mean that the grouping of several variables ORed or ANDed together does not matter.
- Distributive law: $A(B + C) = AB + BC$, and $(A + B)(C + D) = AC + AD + BC + BD$.
These laws show methods for expanding an equation containing ORs and ANDs.

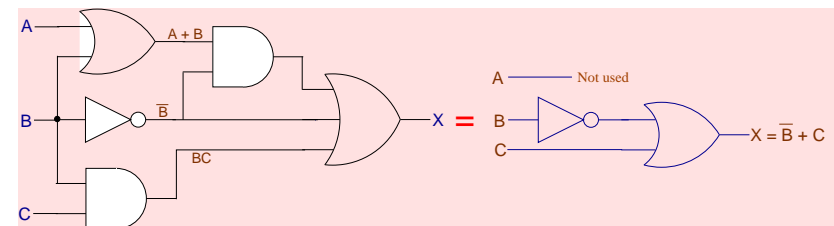


Boolean Algebra Rules

- Anything ANDed with a 0 is equal to 0 ($A \cdot 0 = 0$).
- Anything ANDed with a 1 is equal to itself ($A \cdot 1 = A$).
- Anything ORed with a 0 is equal to itself ($A + 0 = A$).
- Anything ORed with a 1 is equal to 1 ($A + 1 = 1$).
- Anything ANDed with itself is equal to itself ($A \cdot A = A$).
- Anything ORed with itself is equal to itself ($A + A = A$).
- Anything ANDed with its own complement equals 0 ($A \cdot \bar{A} = 0$).
- Anything ORed with its own complement equals 1 ($A + \bar{A} = 1$).
- A variable that is complemented twice will return to its original logic level ($\bar{\bar{A}} = A$).
- (a) $A + \bar{A}B = A + B$
(b) $\bar{A} + AB = \bar{A} + B$



Reduction of Logic Circuits: Example

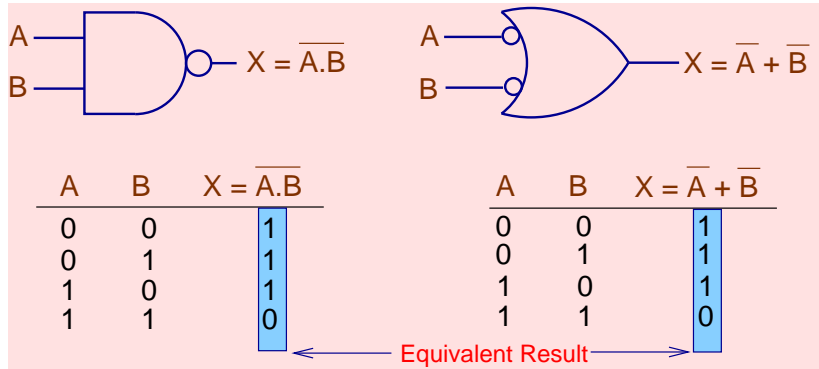


$$\begin{aligned}
 X &= (A + B)\bar{B} + \bar{B} + BC \\
 &= A\bar{B} + B\bar{B} + \bar{B} + BC && \text{Law 3} \\
 &= A\bar{B} + 0 + \bar{B} + BC && \text{Rule 7} \\
 &= A\bar{B} + \bar{B} + BC && \text{Rule 3} \\
 &= \bar{B}(A + 1) + BC && \text{Factorisation} \\
 &= \bar{B} \cdot 1 + BC && \text{Rule 4} \\
 &= \bar{B} + BC && \text{Rule 2} \\
 &= \bar{B} + C && \text{Rule 10(b)}
 \end{aligned}$$

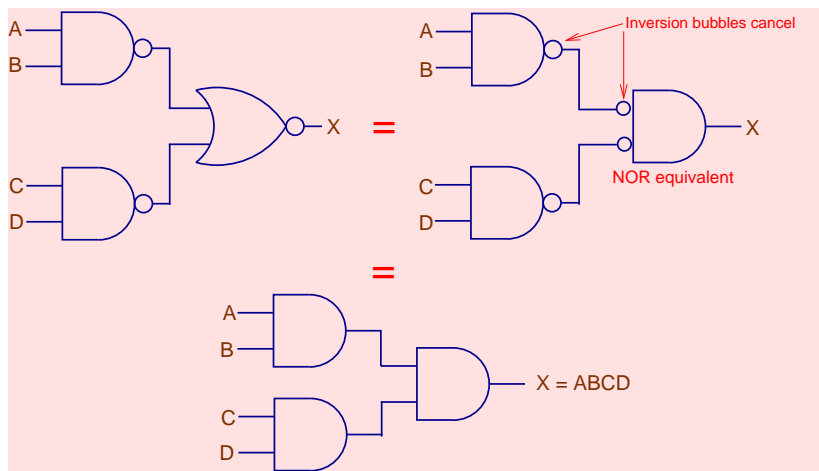


De Morgan's Theorem 1

$$\overline{A \cdot B} = \overline{A} + \overline{B}$$

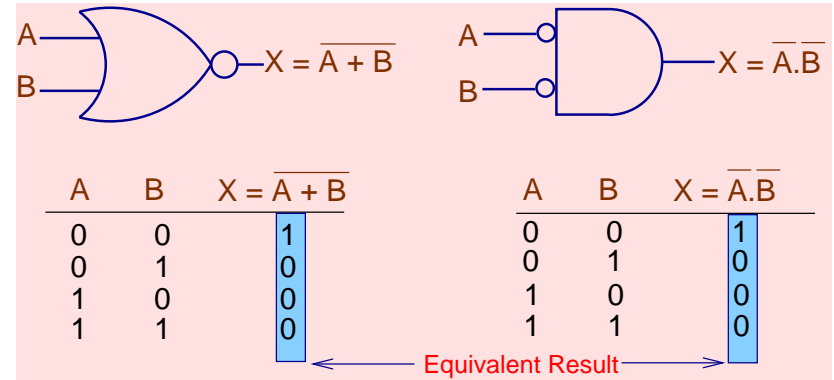


De Morgan's Theorem: Application

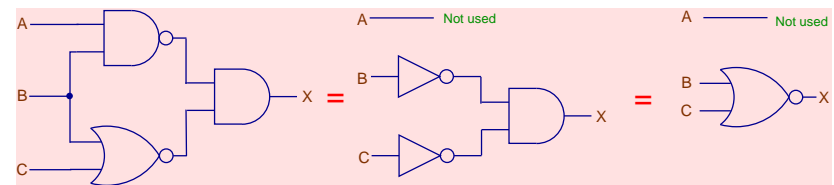


De Morgan's Theorem 2

$$\overline{A + B} = \overline{A} \cdot \overline{B}$$



De Morgan's Theorem: Application



$$\begin{aligned}
 X &= \overline{A \cdot B} \cdot \overline{B \cdot C} \\
 &= (\overline{A} + \overline{B}) \cdot (\overline{B} + \overline{C}) \\
 &= \overline{A} \overline{B} \overline{C} + \overline{B} \overline{B} \overline{C} \\
 &= \overline{A} \overline{B} \overline{C} + \overline{B} \overline{C} \\
 &= \overline{B} \overline{C} (\overline{A} + 1) \\
 &= \overline{B} \overline{C} \\
 &= \overline{B + C}
 \end{aligned}$$

De Morgan's theorem

